

## WEST Search History





DATE: Tuesday, September 21, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L19	L16 and (rtl same (processor or simulator))	7
<input type="checkbox"/>	L18	L17 and (rtl same simulat\$)	7
<input type="checkbox"/>	L17	L16 and (functional same state)	8
<input type="checkbox"/>	L16	l13 and L15	10
<input type="checkbox"/>	L15	functional same compil\$	2675
<input type="checkbox"/>	L14	L13 and silicon	19
<input type="checkbox"/>	L13	L12 and (hdl or vhdl)	33
<input type="checkbox"/>	L12	l5 and L11	33
<input type="checkbox"/>	L11	rtl and peripheral and compiler	187
<input type="checkbox"/>	L10	L9 and rtl	40
<input type="checkbox"/>	L9	L8 and state and processor	49
<input type="checkbox"/>	L8	L7 and (hdl or vhdl)	55
<input type="checkbox"/>	L7	L5 and silicon and peripheral	98
<input type="checkbox"/>	L6	L5 and ((hdl or vhdl) same peripheral same simulation)	9
<input type="checkbox"/>	L5	functional same model same simulation	815
<input type="checkbox"/>	L4	L3 and peripheral and simulation and ((high or low) same level)	2
<input type="checkbox"/>	L3	l1 or L2	21
<input type="checkbox"/>	L2	5995736	8
<input type="checkbox"/>	L1	5557774	14

END OF SEARCH HISTORY